

RESUME

Name : PRISCILLA SCARLET S
E-mail address : priscillascarlet@yahoo.com
Contact number : +919445677620



CAREER OBJECTIVE:

Looking forward to build my career by utilizing all the opportunities to implement my skills and knowledge in order to pursue a long term relationship with the organization.

ACADEMIC PROFILE

Course	Board of study	Year of passing	Percentage
XII	St.John'S MHSS, Villivakkam, Chennai State Board	2004	88.8
X	K.V.H.V.F Avadi, Chennai CBSE	2002	88.7

PROFESSIONAL QUALIFICATION

PhD in Nanoelectronics, SSN College of Engg.(Affiliated to Anna University), Chennai, 2018

Dissertation Title: "Performance analysis of junctionless devices on planar and non planar structures"

M.E. VLSI Design, SSN College of Engg.(Affiliated to Anna University), Chennai, 2013

Percentile: 8.4

B.E. Electronics and Communication Engg., S.A Engineering College, Chennai, 2008

Percentage: 80.5

ACHIEVEMENT

*School third in Tenth standard CBSE board examination.

***Gate 2013** qualified with **95 percentile**

***NET 2019** Qualified with **98.4 percentile**

SKILLS

- **Design and Simulation Tools:** Synopsys TCAD, Matlab, Xilinx, Proteus, Cadence
- **Programming:** VHDL, Verilog, C
- **Operating system:** Windows

ACADEMIC EXPERIENCE

- Nanoelectronics for B.E
- VLSI Design for B.E
- Basic Electronics for B.E
- ASIC for M.E
- Digital modeling using HDL for B.E
- Semiconductor Technology for M.E

PUBLICATIONS

- **Scarlet, S, P, Vinodhkumar and Srinivasan, R** 2020, ‘Performance Enhancement of Junctionless Silicon Nanotube FET using Gate and Dielectric Engineering’, Journal of Computation Electronics (**SCI indexed – Springer with Impact factor of 1.532**).

Available online:

<https://doi.org/10.1007/s10825-020-01611-5>

Number of Citations-1

- **Scarlet, S, P, Ambika, R, and Srinivasan, R** 2017, ‘Effect of eccentricity on junction and junctionless based silicon nanowire and silicon nanotube FETs’, Superlattices and Microstructures., vol. 107, pp. 178–188.(**Elsevier-Thompson Reuters indexed with Impact factor of 2.12**)

Available online :

<http://www.sciencedirect.com/science/article/pii/S0749603617308327>

Number of Citations-12

- **Scarlet, S, P, Prasannanjaneyulu, B, and Srinivasan, R** 2017, ‘Performance Optimisation of Junctionless FET in Nano regime using Segmented Channel- A 3D Numerical simulation study’, Superlattices and Microstructures., vol. 111, pp. 1233–1243.(**Elsevier -Thompson Reuters indexed with Impact factor of 2.12**)

Available online:

<https://doi.org/10.1016/j.mssp.2018.05.009>

Number of Citations-1

- **Scarlet, S, P and Srinivasan, R**, “Optimization of nanometer bulk junctionless Trigate FET using gate and isolation dielectric engineering,” Material Science in Semiconductor processing, vol. 84, no. March, pp. 107–

114, 2018. (Elsevier- Thompson Reuters indexed with Impact factor of 3.085)

Available online:

<http://www.sciencedirect.com/science/article/pii/S0749603617312053>

Number of Citations-7

- **Scarlet, S, P,** Srinivasan, R and Sasikala, P 2017, 'Nanoscale Junctionless Devices Using RingFET Structure on Bulk Silicon Substrate', International Journal of Electronic Letters (Scopus indexed with Taylor and Francis).

Available online :

<http://www.sciencedirect.com/science/article/pii/S0749603617312053>

Number of Citations-1

CONFERENCES

- Scarlet, P, K. K. Nagarajan and R. Srinivasan, "Optimization of doping profile and isolation oxide thickness in bulk FinFETs using TCAD simulations," *2013 International Conference on Circuits, Power and Computing Technologies (ICCPCT)*, Nagercoil, 2013, pp. 959-965.

Number of Citations -4

- Scarlet, S, P, and Srinivasan, R., " Impact of Doping and Spacer on the Performance of Bulk Planar Junctionless devices", *2018 4th International Conference on Electrical Energy systems (ICEES 2018)*, Chennai.

Number of Citations -1

WORKSHOPS PARTICIPATED

- Attended workshop on "Analog and Digital IC design using Cadence tools" at Easwari College of Engineering Ramapuram, on July 2014.
- Attended workshop on "Cadence tools" at SSN College of Engineering Kalavakkam, on March 2015.
- Attended workshop on "Silvaco Full Flow TCAD/EDA Design tools" at RVS College of Engineering Bangalore, on April 2015.
- Attended workshop on "NEMS and Nanophotonics" at IIT Madras, on September 2015.
- Attended workshop on "Modeling, Simulation and Characterization of Nano-Transistors" at IIT Kanpur, on October 2015.
- Attended workshop on "Physics of Semiconductor Devices (IWPSD-2015)" at IISc Bangalore, on December 2015.
- Attended workshop on "INUP Familiarization workshop on nanofabrication technologies" at IIT Bombay, on December 2015..

EMPLOYMENT HISTORY:

Name of the Organisation	Designation	From	To	Duration (Years)
Oracle Financial Services Software	Associate Consultant In Universal Banking Development	Oct 2008	Aug 2011	2.8
SSN College of Engineering	JRF	Feb 2014	Feb 2017	3
SSN College of Engineering	SRF	Jan 2018	June 2018	5 months
KL University	Assistant Professor	June 2018	June 2019	1 year
CVR College of Engineering	Assistant Professor	July 2019	August 2022	3 years 1 month

REFERENCE

Dr. R. Srinivasan,
Professor,
Department of Information Technology,
SSN College of Engineering, Kalavakkam,
Chennai, India
Mobile No: +91-9444446747
Email id: srinivasanr@ssn.edu.in

DECLARATION

I hereby declare that the above furnished details are true to the best of my knowledge.

Place:
Bilaspur, Chattisgarh.

Dr S. Priscilla Scarlet