



*Department of Electronics & Communication Engineering
School of Studies of Engineering & Technology
Guru Ghasidas Vishwavidyalaya, Bilaspur (CG)*





**1 Professor, 7 Associate Professor,
12 Assistant Professor**

**Publications 360+,
Highest H-index=22**

**06 Books, 60 chapters,
130 Conferences**

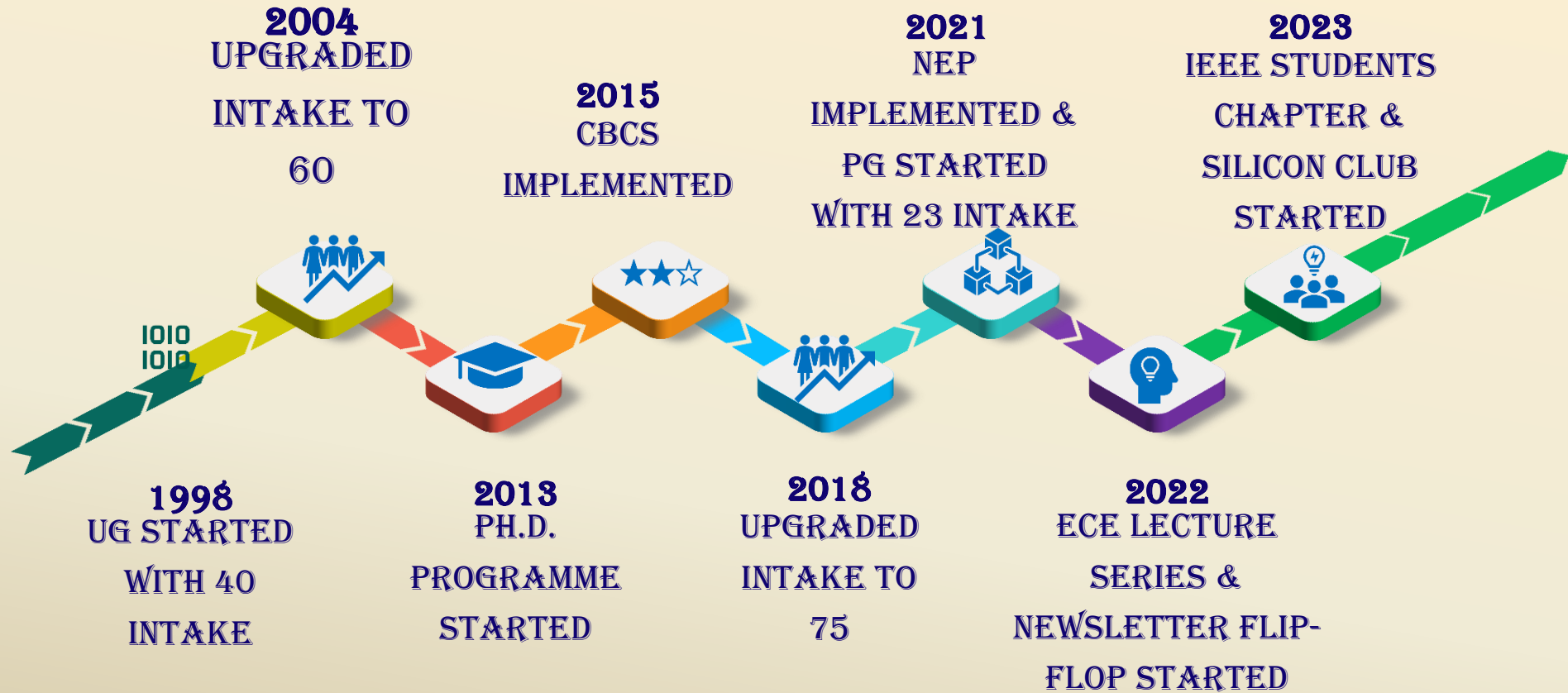


**Alumni: 2 IES, 16+
GoI officials**

**75 UG (2018)
23 PG Intake**

**MOU with
Raipur Smart City**

Milestones....





Vision of the Department

The Department endeavours for academic excellence in Electronics & Communication Engineering by imparting in depth knowledge to the students, facilitating research activities and cater to the ever-changing industrial demands, global and societal needs with leadership qualities.

Mission of the Department

- *To be the epitome of academic rigour, flexible to accommodate every student and faculty for basic, current and future technologies in Electronics and Communication Engineering with professional ethics.*
- *To develop an advanced research centre for local & global needs.*
- *To mitigate the gap between academia, industry & societal needs through entrepreneurial and leadership promotion.*



Department Highlights

- Students outreach activities: IEEE student branch, Students Club SILICON, Publishing departmental Newsletter The FLIP-FLOP
- Students progressed in higher studies from IITs & reputed Universities of US, EU and other countries
- Students received internship with fellowship & placement, FACT fellowship from GoI



Oct-Dec-22, Volume-1 , Issue -4

MOU, Industrial Visits & Interactions



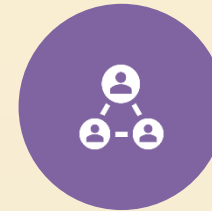
Teaching Learning Process



**Regular
implementation &
revision of course
curriculum**



**Test & exams
(2 CT & 1 ESE)**



**Exposure to universal
human values (Trained
teachers)**



Mentor-mentee



**Exposure to
experiential learning**

Experiential Learning



Laboratory classes of respective subjects & demonstration of equipment,

Minor & Major Project implementation,

Conduction of Seminar classes,

Assignments, Tutorials & Group discussions,

Exposure to universal human values under the curriculum,

Remedial classes based on the assessment of slow/fast learners,

Regular Visit to Industry in final & pre-final year students,

Online Internship Program (Optional),

Workshop, & career counseling for higher studies.

Industrial Training



Identification of Slow and Fast Learners



At the entry-level, the basis is 12th standard marks



The remedial classes are arranged for slow learners



At Higher levels based on CT-1 Marks/Last semester marks



Additional resources and guidance are provided for Fast learners

The team



Dr. Soma Das, Professor
Antenna & RF component
Design



Dr. Anita Khanna, Assoc. Professor
Signal Processing, Image processing,
Machine Learning, Deep Learning



Dr. P K Sethy, Assoc. Professor
Image processing, Machine
Learning, Deep Learning



Dr. S S Chauhan, Assoc. Professor
Low power VLSI device, Wireless
Comm., Physical Layer Security



Dr. N K Mishra, Assoc. Professor
Antenna Design, Metamaterial
Absorber, RF & Microwave



Dr. R Dey, Assoc. Professor
Embedded System, IoT,
Linear and Adaptive Control



Dr. P S Shrivastava, Assoc. Professor
Wireless Comm., D-2-D Comm., 5G &
beyond Comm System



Dr. M Gupta, Assoc. Professor
AI, Machine Learning, Deep Learning,
Biomedical Signal & Image
processing/Medical Imaging



Bhawna Shukla, Asstt. Professor
Wireless Communication



Beaulah Nath, Asstt. Professor
Digital VLSI

The team



S K Patel, Asstt. Professor
Microwave Antenna, RF Beamformers,
Microwave Circuits



D K Rathore, Asstt. Professor
Wireless Communication,
Communication Networks,
Massive MIMO systems



Dr. N Kashyap, Asstt. Professor
Image processing, Artificial Intelligence.
Machine learning



S K Gupta, Asstt. Professor
Microwave Structures,
Metamaterial, Power Circuits



J Bharadwaj, Asstt. Professor
Computer Vision & Applied AI, Robotics,
Digital Signal Processing & Intelligent
Automation & Multi-Media Applications



Dr. A K Soni, Asstt. Professor
Image processing, THz Device Design



P Patharia, Asstt. Professor
Opto electronics



P Rajput, Asstt. Professor
Electronics Devices, Network
Security, Communication, VLSI



C Tamrakar, Asstt. Professor
Communication Signal
processing, Biomedical Signal
Processing (ECG Signal)



Dr. R Tripathi, Asstt. Professor
AI/ML for 5G Communication

Office Staff = 01

Technical Staff = 03

Research Scholars = 12



- ❖ **Editorship: Taylor & Francis, Wiley, Apple Academic Press, International Journal of Science, Technology and Society**
- ❖ **Research Copyright = 03**
- ❖ **Total Journal Publication = 360**
- ❖ **Highest h-index = 22**
- ❖ **No. of Patent = 35 (08 granted)**
- ❖ **No. of Books = 06, chapters = 60**
- ❖ **Conference attended = 130**
- ❖ **Research Project ongoing/completed: 3/1**
- ❖ **International Higher Education Excellence Award 2023: Dr. Manoj Gupta**
- ❖ **Reviewer : Elsevier, IETE, IEEE Transactions on signal Processing, WILEY**
- ❖ **Key note Speaker, Session Chair, Expert Lecture: 20+**



Faculty Achievements, Awards & Publications

Major Recruiters



SYNOPSYS



Success Stories



Training and Placement Cell,
School Of Studies of Engineering &
Technology



Batch
2023

Congratulations on scoring well
in GATE Examination



AIR: 912
PAPER: EC
TANGUDU SAI PAVAN
ECE | 2023 BATCH |



AIR: 3406
PAPER: EC
ASHUTOSH KUMAR
ECE | 2023 BATCH |



AIR: 4018
PAPER: EC
DEEPANSHU PATEL
ECE | 2023 BATCH |



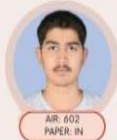
AIR: 5285
PAPER: EC
SAURABH GUPTA
ECE | 2023 BATCH |



AIR: 5705
PAPER: EC
GIRIRAJ GAUTAM
ECE | 2023 BATCH |



AIR: 6487
PAPER: EC
SADASIVUNI SAI RAHUL
ECE | 2023 BATCH |



AIR: 602
PAPER: IN
YASH SINGH CHAUHAN
ECE | 2023 BATCH |



AIR: 1160
PAPER: AE
K. S. PRABHATH BHUSHAN VARMA
ECE | 2023 BATCH |



गुरु घासीदास विश्वविद्यालय, बिलासपुर

Guru Ghasidas Vishwavidyalaya, Bilaspur

A Central University established by the Central University Act 2009 No. 25 of 2009

S.N.	Name	Company
1	Rishabh Upadhyay	Hexaware Tech
2	K Shyam P B Verma	Hexaware Tech
3	Sagam Narsimham	TCS
4	Amit Mishra	High-Tech Next
5	Anish Kumar	High-Tech Next
6	Koushik Ghosh	High-Tech Next
7	Murshid Raza	High-Tech Next
8	Praveen Thakre	High-Tech Next
9	Virendra Yadav	High-Tech Next
10	Prince Jaiswal	Tech Mahindra
11	Prince Jaiswal	TCS
12	Reddy Akhila	Learning Routes

Success Stories



Batch
2024

Congratulations on qualifying
the GATE exam!



NISHANT WANKHADE
GATE SCORE 643



M D RAJU
GATE SCORE 410



P.N REDDY
GATE SCORE 390



G SANDEEP
GATE SCORE 320



S SAI NITHIN
GATE SCORE 320



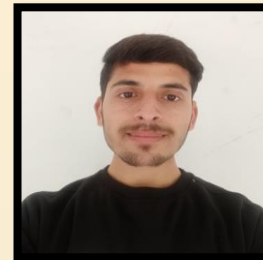
R. SAI PRASAD
GATE SCORE 380



Success Stories



➤ *Tractrix Opto Dynamics offered paid internships to: Srijani Som, Ankit Kumar, Harsha Verma.*



➤ *Nishant Wankhade received an Internship offer from the Department of CSE, IIT Bombay.*



➤ *Rishabh Upadhyay participated in the Floral Tribute function with PM at New Delhi on 23 Jan 2023.*

➤ *Five students have completed the FACT Internship of ABPVA with a stipend.*



Success Stories



GPS Map Camera

Bilaspur, Chhattisgarh, India

39, Shiv Mandir Rd, Chandela Vihar, Opp, Agay nagar, Bilaspur,

Chhattisgarh 495001, India

Lat 22.075069°

Long 82.143371°

02/08/23 10:10 AM GMT +05:30

Google

<i>Name of Students</i>	<i>Stipend (in Rs)</i>
<i>SANKARSHAN MISHRA</i>	<i>1,00,000</i>
<i>GAURAV KUMAR</i>	<i>50,000</i>
<i>SANA PAVANI</i>	<i>35,000</i>
<i>R P RAMACHANDRAN</i>	<i>25,000</i>
<i>NEHA SAHU</i>	<i>15,000</i>

Existing Facilities



GURU GHASIDAS VISHWAVIDYALAYA
A CENTRAL UNIVERSITY |BILASPUR CHHATTISGARH

ECE LECTURE SERIES 2023-24

Talk on
How to Join ISRO and Other Career Prospects
19th Dec 2023 at 12:00 noon
Venue: E-CLASS ROOM



Shreeti Goyal
Alumni ECE 2005
ISRO Scientist (URSC)

Chief Patron
Prof. Alok K. Chakrawal
Vice Chancellor

Patron
Prof. Manish Shrivastava
Registrar

Co-Patron
Prof. S. C. Srivastava
Dean (SOS E&T)

Convener
Dr. Anita Khanna
(Associate Prof.)


Head of Department
Prof. Soma Das
Prof. ECE

Organized By:
Department of Electronics & Comm. Engineering

GURU GHASIDAS VISHWAVIDYALAYA
A CENTRAL UNIVERSITY |BILASPUR CHHATTISGARH

ECE LECTURE SERIES 2023-24

She will talk on
How to Prepare for GATE and Build your Career.
12th July 2023 at 11:00 AM
Venue: E-CLASS ROOM



SHRESHATHA GUPTA
Alumni ECE 2021
Graphic Hardware Engineer at Intel

Chief Patron
Prof. Alok K. Chakrawal
Vice Chancellor

Patron
Prof. Manish Shrivastava
Registrar

Co-Patron
Prof. S. C. Srivastava
Dean (SOS E&T)

Convener
Dr. Soma Das
HOD ECE

GURU GHASIDAS VISHWAVIDYALAYA
A Central University, Bilaspur, Chhattisgarh

ECE LECTURE SERIES

February 24, 2022 | 10:00 AM

Alumni Guests :



Mr. Deepak Verma
Sr. Tech & Regional Manager in **MediaTek**
(Alumni 2002)
Topic : Scope of ECE in India...
Skills required for cracking a core electronics company



Mr. Chandra Nandipati
Technical Architect in **"Intercontinental Exchange,NYSE", US**
(Alumni 2015)
Topic : Future Opportunities

Chief Patron
Prof. Alok K. Chakrawal
Vice Chancellor

Patron
Prof. Shailendra Kumar
Registrar

Co-Patron
Prof. T.V. Arjunan
Dean (SOS E&T)

Convener
Dr. Soma Das

Coordinator
Mrs. Bhawna Shukla

Coordinator
Mrs. Praveena Rajput

Date of Event : March 13, 2023

Venue : E- Classroom, School of Studies (Engineering & Technology), Bilaspur



Mr. Chandra Nandipati Delivering Lecture on 13-03-2023

GURU GHASIDAS VISHWAVIDYALAYA
A CENTRAL UNIVERSITY |BILASPUR CHHATTISGARH

ECE LECTURE SERIES

Speaker
SWAMI SEVAVIRATANANDAJI
Secretary Ramkrishna Mission Bilaspur

18th Jan, 2022

Department of Electronics and Communication Engineering
School of Studies of Engineering & Technology

Bilaspur, Chhattisgarh, India

Department Of Pharmacy GGV Rd, Konl, Bilaspur, Chhattisgarh
Lat 22.131884°
Long 82.141937°
18/07/22 12:23 PM

ECE LECTURE SERIES

Alumni Guests :



Mr. VIKAS SWARNKAR
Scientist/Engineer- SF, Deputy Head, System Reliability, Sriharikota AP(Alumni 2004)



Mr. Abhishek Khandelwal
Sr. Tech member of technical staff-I into R&D team for Ex – Saksen, Nokia in 4G/5G domain. An entrepreneur (Alumni 2015)

Chief Patron
Prof. Alok K. Chakrawal
Vice Chancellor

Patron
Prof. Shailendra Kumar
Registrar

Convener
Dr. Soma Das
HOD ECE

Coordinator
Mrs. Bhawna Shukla
Asst. Prof. ECE

Organised by :
Department of Electronics and Communication Engineering
School of Studies



Skill Enhancement



GURU GHASIDAS VISHWAVIDYALAYA, BILASPUR
A CENTRAL UNIVERSITY ESTABLISHED BY THE CENTRAL UNIVERSITY ACT 2009 NO. 25 OF 2009

IEEE GGV
A Student Branch

SOCIETY FOR INTEGRATED LEARNING
COMMUNICATION AND NETWORKING
GURU GHASIDAS VISHWAVIDYALAYA, BILASPUR

Training session on embedded system by INNOVATION4U

VENUE : E-CLASSROOM
DATE : 3rd May, Wed.
TIME : 10:00 AM



Y20
2023-2024

IEEE
Advancing Technology
for Humanity

गुरु घासीदास विश्वविद्यालय, बिलासपुर
GURU GHASIDAS VISHWAVIDYALAYA
A CENTRAL UNIVERSITY ESTABLISHED BY THE CENTRAL UNIVERSITY ACT 2009 NO. 25 OF 2009

Inauguration of

IEEE GGV STUDENT BRANCH SILICON CLUB

April 06, 2023
Venue : E-classroom, Time : 19:00

Department of Electronics and Communication Engineering
School of Studies of Engineering

Patron
Prof. Manish Shrivastava
Registrar

Guest of Honor
Dr. R.N. Patel
IEEE Senior member

Guest of Honor
Shubham Gupta
SAC Chair IEEE M.P. Section

Guest of Honor
Dr. Vijaylakshmi Bhandari
Chairperson WIE AG IEEE M.P. Section

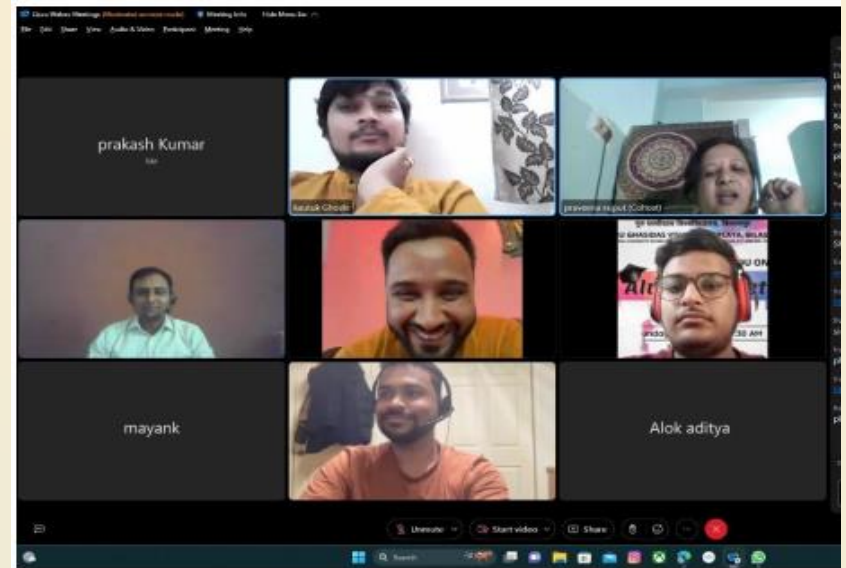
Counselor
Dr. Anil Kumar Soni
IEEE Student Branch, GGV

Teacher Coordinator SILICON CLUB
Praveena Rajput



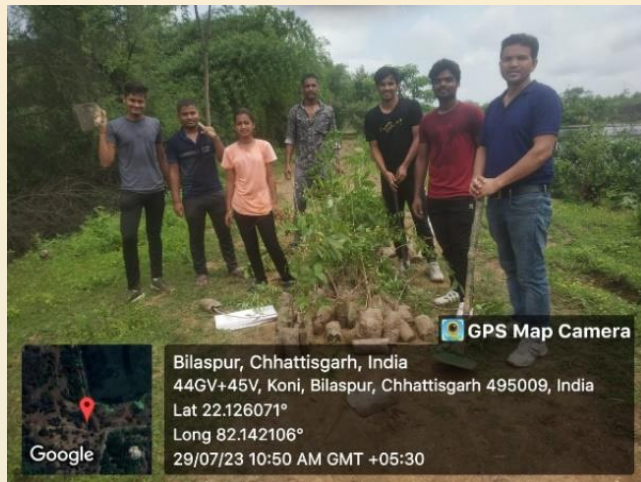
Alumni Meet

- Department conducted the online Alumni meet on 07th May 2023.
- 1st to 4th year B.Tech, M.Tech, PhD students participated in the meet.
- Discussed on the current placement scenarios, Startups, Higher Studies.



Alumni Meet on 07 May 2023

In Harmony with Nature



SCHOOL OF STUDIES ENGINE & TECH, GURU GHASIDAS VISHWAVIDYALAYA
EQUILIBRIO'22
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



ARUN KUMAR
Director, Sincgrid LLP, Delhi

Graduate in Electronics and communication Engineering from NSIT Delhi
 Major devotion to hardware development for embedded and low power application.
 Experienced in developing educational platforms for hands on experimentation.

ARDUINO COMPONENTS KIT PURCHASE LINK
<https://bit.ly/3135y6w>

WORKSHOP ON ARDUINO
 By ECE Tech Team
 18th February

SCHOOL OF STUDIES OF ENGINEERING & TECHNOLOGY
GURU GHASIDAS VISHWAVIDYALAYA, BILASPUR (C.G.)
RESEARCH & DEVELOPMENT COMMITTEE PRESENTS WEBINAR ON

ART OF WRITING RESEARCH PAPER



Dr. Gaurav Varshney
 Assistant Professor, Department of Electronics and Communication Eng
 National Institute of Technology Patna

Date & Time- 7th November 2020, Saturday at 11 AM to 2 PM
 There is no registration fee for the participants. E-certificate will be provided to the participants.
 Registration Link: <https://forms.gle/RwJpZqqevhZqf3oA>

CHIEF PATRON
 Prof. Anjita Gupta,
 Vice Chancellor
PATRON
 Prof T.V. Arjunan,
 Dean (SoS E&T)

RESEARCH CO-ORDINATOR
 Dr Jasinta Poonam Ekka
CO-ORDINATOR
 Dr. Anil Kumar Soni
 Dr. Ghoshna Jyoti

ORGANIZING COMMITTEE
 Dr. Kundan Meshram
 Dr. Anoop Sahu
 Mrs. Disha Dewangan
 Mrs. Princy Matla
 Mr. Amit Kumar Dewangan

Week Online Workshop on
SKILL DEVELOPMENT FOR TECHNOLOGY TRANSFER
 Organized by
 Research & Development Cell, SoS (E&T) and
 Skill Development Cell GGV, Bilaspur
 13-14 Sep. 2021 - 18 Sep. 2021

About Speakers
 Our speakers/experts from various NITs and prestigious institutes. They will share their knowledge to develop participants skills towards technology transfer.

 Dr. Sandeep Kumar Professor, NIT Hyderabad	 Mr. Rajat Malhotra Partner, Ideaz2LP
 Dr. Shilpa Choudhary Associate Professor, NIT Hyderabad	 Dr. R. N. Patel Associate Professor, IIT Bombay
 Dr. Md. Rasid Mahmood Professor, GNI Hyderabad	

Register Now
<https://forms.gle/AN7xWw9d9EN5d1yZu9>

Contact Details
 Mr. Amit Kumar Dewangan (9691136968)
 Dr. Kundan Meshram (9424906324)
 Dr. Anil Kumar Soni (9445220789)

SKILL-DEVELOPMENT CELL, GGV-BILASPUR
 presents
A Gateway for Young Researchers

One week Online Seminar Series On
Current Trends in Engineering Projects
 (A project prospects to UG & PG students)
 25 Nov 2020 - 29 Nov 2020

Organized by
 Department of Electronics & Communication Engineering
 and Skill Development cell
 GGV, Bilaspur

ABOUT OUR SPEAKERS
 Various speakers/experts from NITs and various prestigious institutes will be invited. They will share the knowledge on these areas to motivate the participants to start their research career in these fields.

 Dr. Gaurav Varshney Asst. Professor, NIT Patna	 Dr. Sita Rani Patra Asst. Professor, NIT Boudh
 Dr. Binuaji Diviedy Asst. Professor, VIT Vellore	 Dr. Ranjit Kumar Asst. Professor, VIT Chennai
 Dr. Koushik Kumar Singh Asst. Professor, NIT Jamshedpur	

REGISTER NOW
<https://forms.gle/2k4kx275d5v8B8fG6>

Contact Details
 Dr. Anil Kumar Soni
 Contact No: 942430789
 Email: anilsoni@rediffmail.com

Dr. Shivam Kumar Patel
 Contact No: 9424133622
 Email: shivampatel@rediffmail.com

Last Date of Registration : 23rd Nov 2020 & Confirmation of Participants : 24th Nov 2020

ATAL
CTE Training and Learning (TAL) Academy Sponsored One Week Online Faculty Development Program (FDP) On

Services for Modern Wireless Communication Systems*

From 20th to 24th September 2021

Organized By :
 Institute of Electronics & Communication Engineering,
 School of Studies (Engineering & Technology)
 Guru Ghansidass Vishwavidyalaya (A Central University),
 Bilaspur, C.G., India

CHIEF PATRON

Prof. Alok Kumar Chakraborty
 Associate Vice-Chancellor,
 GGV Bilaspur

PATRON

Prof. Shalendra Kumar
 Registrar, GGV Bilaspur

CONVENER

Prof. T. V. Arjunan
 Dean SoS (E & T) & Head
 Department of Mechanical Engineering

CO-ORDINATOR

Dr. Vijay Kumar Mishra
 Asst. Prof. (EIT)
 GGV Bilaspur

CO-COORDINATOR

Mr. Chandan Tamarak
 Asst. Prof. (EIT)
 GGV Bilaspur

Registration Process

1. No registration fee will be charged from faculty and students.
2. Registrations can be done online using the link: www.atal.academy.org/
3. Last date of registration is 19th September 2021.
4. Joining link for the online session will be shared to the registered participant on their Email.

About GGV Bilaspur
 Guru Ghansidass Vishwavidyalaya is a Central University, located in Bilaspur C.G. Zone, established under Universities Act 2009, No. 25 of 2009. Formerly called Ghansidass University (GVSU), established by an Act of H. Legislative Assembly, was formally inaugurated on June 1, 1960. It is an active member of the Association of Universities and Association of Commonwealth Universities. Ghansidass is a socially and economically challenged university is appropriately named to honor the great Saint Guru Ghansidass (born in 17th century), who changed the state of the disadvantaged and inspired a renaissance through all forms of social evils and injustice prevailing in the area.

About ATAL Academy
 All India Council for Technical Education (AICTE) Its newly established ASCTE Training and Learning Academy have started unique faculty development programme through areas of modern technology. ATAL, successfully conducted 5 days face to face 150 FDP Development program (online thrust area) for AICTE, approximately 50000 faculty members. In 2020-21, 100 FDP are being conducted and more than one lakh participants already participated including faculty, Research scholars, B.Tech teachers and industry persons. The year 2021 has been recognized as a world record by World Record, London. This is also important that FDP was recorded and available on portal so that anyone can see future. ATAL Academy have also included 15 FDPs on learning and flipped classroom which is very important covid-19 scenario as to teach participants how to conduct in virtual mode and make them comfortable taking online and assignments. In the backdrop of announcement of Education Policy (NEP) 2020, ATAL Academy is working in direction of NEP, keeping in view the value and merit of Education System. This is the largest online FDP program where more than 40% female participants have joined.

CHIEF PATRON

Prof. Alok Kumar Chakraborty
 Associate Vice-Chancellor, GGV Bilaspur

PATRON

Prof. Shalendra Kumar
 Registrar, GGV Bilaspur

CONVENER

Prof. T. V. Arjunan
 Dean SoS (E & T) & Head
 Department of Mechanical Engineering

Resource Persons


Dr. F. KANAGAVEL
 Director's Head
 National Institute of Wind Energy (NIWE) Ministry of New and Renewable Energy, Govt. of India


Dr. SUBRAMANIAN
 Department of Bioenergy
 Tamil Nadu Agricultural University (TNAU)
 Tamil Nadu

Tonic - Recent Development in Wind Energy
Topic - Advances in Biomass Conservation Technologies

Events@ECE

Online Faculty Development Workshop
Prof. T.V. Arjunan
Dean, SoS E&T
GGV Bilaspur, Chhattisgarh

Organized by
 School of Studies in Engineering & Technology,
 Guru Ghansidas Vishwavidyalaya (A Central
 University), Bilaspur, C.G. India

In Association with
 Indian Institute of Technology, Kharagpur, West
 Bengal, India

Initiative of
 Ministry of Human Resources & Development
 (MHRD), Govt. of India

About GGV Bilaspur
 Guru Ghansidas Vishwavidyalaya is a Central
 University of India, located in Bilaspur C.G. State,
 established under Central Universities Act 2009,
 No. 23 of 2009. Formerly called Guru
 Ghansida University (GGU), established by an
 Act of the State Legislative Assembly, was
 inaugurated on June 16, 1983. GGU is an
 active member of the Association of Indian
 Universities and Association of Commonwealth
 Universities. Situated in a socially and
 economically challenged area, the university is
 appropriately named to honor the great Saint
 Saint Gurus (born in 17th century), who
 championed the cause of the downtrodden and
 waged a relentless struggle against all forms of
 social evils and injustice prevailing in the
 society. The campus is lush green, calm and
 quiet and free from urban noise and pollution, an
 ideal temple for education and research.

Chief Patron
 Prof. Alok Kumar Chakraborty,
 Patron
 Prof. Shailendra Kumar, Registrar, GGV
 Convenor
 Prof. T.V. Arjunan, Dean (SoS E&T), GGV
 Coordinator
 Dr. Anil Kumar Soni, GGV
 Co-coordinator
 Mr. Pankaj Kumar Chandra, GGV

CHIEF PATRON
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 Patron
 Prof. Shailendra Kumar
 Registrar
 Prof. T.V. Arjunan
 Dean (SoS E&T), GGV
 Convenor
 Prof. T.V. Arjunan
 Coordinator
 Dr. Anil Kumar Soni
 Co-coordinator
 Mr. Pankaj Kumar Chandra

OUTCOME BASED CURRICULUM DESIGN
 September 03-04, 2021

Organized by
 Dept. of Electronics & Communication Engineering
 School of Studies in Engineering & Technology
 (In a Remote Center)
 Guru Ghansidas Vishwavidyalaya
 Bilaspur, Chhattisgarh, India

Online Webinar on Quality Assurance with NBA Accreditation
 13th November 2021 (Time: 11 AM)

Organized By
 Department of Electronics and Communication Engineering

RESOURCE PERSON
Dr. Meenakshi Sood
 Associate Professor
 NITTR, Chandigarh

Organising Committee

Chief Patron
 Prof. Alok Kumar Chakraborty
 Honorable Vice Chancellor
 GGV, Bilaspur

Patron
 Prof. Shailendra Kumar
 Registrar
 GGV, Bilaspur

Convenor
 Prof. T.V. Arjunan
 Dean (SoS E&T)
 GGV, Bilaspur

Coordinator
 Mrs. Anita Khanna
 Head, ECE Department
 GGV, Bilaspur

Co-coordinator
 Dr. Anil Kumar Soni
 Assistant Professor
 GGV, Bilaspur



Webinar on Quality Assurance with NBA Accreditation
 13th November 2021 (Time: 11 AM)

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 Department of Electronics & Communication Engineering

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 NITTR, Chandigarh

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 Dean (SoS E&T)
 GGV, Bilaspur

Coordinator
 Mrs. Anita Khanna
 Head, ECE Department
 GGV, Bilaspur

Co-coordinator
 Dr. Anil Kumar Soni
 Assistant Professor
 GGV, Bilaspur

Events@ECE

Notable Alumni



Vikash Shwarnkar,
Dy Head, System reliability,
SDSC SHAR, ISRO



Dr Sanjeev Kumar
Scientist URSC,
ISRO Bengluru



Vivan Prakash
Scientist /Engineer, SD, ISRO



Ranjeet Deshwal
Commander, Electrical Officer,
Indian Navy



Sumit Kumar
Scientist B, DRDO



Ravi Kant Sahu
Assistant Marshall
Vidhan Shabha C.G.



Ashok Meena
Senior Branch Manager
Union Bank of India



Ankit Dwivedi
Senior Income Tax Officer,
Bangalore

Notable Alumni



Abhishek Shukla
Assitt Divisional Engg
Ministry of Comm., GOI



Shashi Bhusan Singh
Senior Sales Officer
Indian Oil Corp. Ltd.



Dinesh Sharma
UPSC (ESE) AIR-23



Aashish Shukla,
GST Inspector



Tarun Desai
Implementation Specialist,
Upstream Work Software



Arvind Meena Assistant GM,
RBI, Mumbai



Hari Shankar Soni
SDE, BSNL, Raigarh



Robert Mark
Electrical Designer,
Philips



Prahalad Khandekar
Section Engg, Indian
Railway

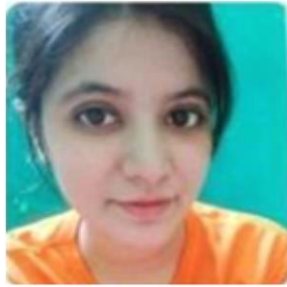


Ambuj Mishra
Senior Product Manager,
Ed Tech Firm

Notable Alumni



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Software Engg,
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Saumya Tripathi
Silicon Engineer
AMD



Dr. Nikita Kashyap
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Manu Kashyap
Senior EM Design Engg,
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Chandra Shekhar Sahu
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Abhishek
Khandelwal
Entrepreneur,
E-commerce



Arvind Gadhwal
Soft Engineer, Microsoft



Mukesh Parihar
Asic Design Engineer, Micron
Technology Hyderabad

Future Road Map



*Welcome to our
Department*

